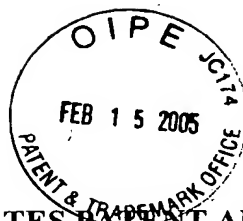


DOCKET NO.: 240471US2S/hyc



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Kenji TSUCHIDA, et al.

GROUP: 2818

SERIAL NO: 10/621,886

FILED: July 18, 2003

EXAMINER: TRAN, Michael Thanh

FOR: MRAM HAVING MEMORY CELL ARRAY IN WHICH CROSS-POINT
MEMORY CELLS ARE ARRANGED BY HIERARCHICAL BIT LINE
SCHEME AND DATA READ METHOD THEREOF

SUBMISSION OF SUPPLEMENTAL APPLICATION DATA SHEET

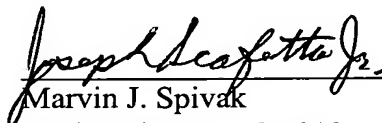
Office of Initial Patent Examination
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicant(s) submit herewith a Supplemental Application Data Sheet for the above-identified application for the purpose of correcting the title which appears incorrectly on the original Application Data Sheet .

Respectfully Submitted,

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SUPPLEMENTAL APPLICATION DATA SHEET

APPLICATION INFORMATION

Application Number:: 10/621,886
Application Date:: 07/18/03
Application Type:: REGULAR
Subject Matter:: UTILITY
CD-ROM or CD-R?: NONE
Title:: MRAM HAVING MEMORY CELL ARRAY
IN WHICH CROSS-POINT MEMORY
CELLS ARE ARRANGED BY
HIERARCHICAL BIT LINE SCHEME
AND DATA READ METHOD THEREOF

Attorney Docket Number:: 240471US2S
Total Drawing Sheets:: 11
Small Entity?: NO

INVENTOR INFORMATION

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Correspondence Customer Number:: 22850

REPRESENTATIVE INFORMATION

Representative Customer Number:: 22850

DOMESTIC PRIORITY INFORMATION

FOREIGN PRIORITY INFORMATION

2003-144792	Japan	05/22/03	YES
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ASSIGNMENT INFORMATION